

JG6/572



ISSUE CLASSIFICATION	
Class	Subclass

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PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED NS4 Q.A. JK

APPLICATION NO. 09/836753	CONT/PRIOR	CLASS 439	SUBCLASS 67	ART UNIT 2833	EXAMINER Leon
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APPlicants

Bobby Self
Clarence Griggs

TITLE

Solder-down printed circuit board connection structures

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<hr/> <hr/>			Amount Due	Date Paid
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